

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **DeWitt et al.**§ Group Art Unit: **Unknown**

Serial No.: _____

§

§ Examiner: **Unknown**

Filed: _____

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§ Attorney Docket No.: **AUS920010713US1**

For: **Method and system using hardware
assitance for instruction tracing with
secondary set of interruption resources**

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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Hon. Assistant Commissioner of Patents

Washington, D.C. 20231

Sir:

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. 1.97.

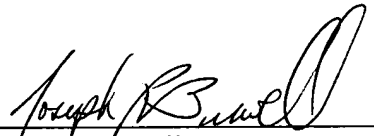
This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee or certification is required.

In accordance with 37 C.F.R. 1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standards of materiality defined in 37 C.F.R. 1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. 1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to the Examiner.

In accordance with 37 C.F.R. 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. 1.56(a) exists.

Date: December 7, 2001


Respectfully submitted,



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Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		ATTORNEY DOCKET NO. AUS920010713US1		SERIAL NO.	
		APPLICANT DeWitt et al.			
		FILING DATE		GROUP ART UNIT	

J0903 U.S. PTO
 10/045537


U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
	5,003,462	03/26/1991	Blaner et al.	362/200	
	5,446,876	08/29/1995	Levine et al.	395/184.01	
	5,642,479	06/24/1997	Flynn	395/183.21	
	5,758,168	05/26/1998	Mealey et al.	395/733	
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	5,944,841	08/31/1999	Christie	714/38	

FOREIGN PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

OTHER PRIOR ART <i>(including author, title, date, pertinent page, etc.)</i>	
	AIX Version 4.3 Kernel and Subsystems Technical Reference, Vol. 1, "pinu Kernel Service", IBM Corp., 1997.
	Itanium™ Processor Microarchitecture Reference for Software Optimization, Intel Corp., 03/2000, pp. 1-28.
	Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 4: Itanium™ Processor Programmer's Guide, Revision 1.1, Intel Corp., 07/2000, pp. 6-1 to 6-32.
	Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 1: IA-64 Application Architecture, Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-8, 3-1 to 3-20, 4-1 to 4-35, 9-1 to 9-16, 10-1 to 10-16.
	Itanium™ IA-64 Architecture Software Developer's Manual, Vol. 2: IA-64 System Architecture, Revision 1.1, Intel Corp., 07/2000, pp. 2-1 to 2-4, 3-1 to 3-22, 5-1 to 5-36, 7-1 to 7-14.

DATE CONSIDERED	EXAMINER
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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		6,145,123	11/07/2000	Torrey et al.	717/4	
		6,173,395 B1	01/09/2001	Wisor et al.	712/236	
FOREIGN PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES NO
	WO	99/54809	10/28/1999	WIPO		
OTHER PRIOR ART <i>(including author, title, date, pertinent page, etc.)</i>						
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